

Teaching Plan: 2025

Subject: Electronics

Paper Name& Paper Code: Digital Electronics &Core Paper A-2

Credit/Total no.ofclasses:3+1=4/75

Period: January 2025—June 2025

Lecture No	Faculty	Topic	Mode of Teaching(Experiential Learning, Participative Learning)	Teaching Aids used	Assessment Method
10	Dr. Mitamoni Sarma	Unit 1: Number system and codes	Lecture method, Notes on important topics, Doubt clearing & Previous year Question paper solving, Group Discussion-P Seminar-P	White Board/ Marker	Class Test, Home Assignment, Seminar
15	Dr. Mitamoni Sarma	Unit 2: Combinational logic design and analysis	Lecture method, Notes on important topics, Doubt clearing & Previous year Question paper solving, Group Discussion-P Seminar-P	White Board/ Marker	Class Test, Home Assignment, Seminar
20	Dr. Mitamoni Sarma	Unit 3: Sequential logic Design Semiconductor memories	Lecture method, Notes on important topics, Doubt clearing & Previous year Question paper solving, Group Discussion-P Seminar-P	White Board/ Marker	Class Test, Home Assignment, Seminar
30	Dr. Mitamoni Sarma	Unit 4: Practical	Practical in Circuit Lab-E	White board/Marker, Digital IC &Electronic Devices	Class Test

**P-Participative Learning

**E-Experiential Learning